

Wiener Filtering Applied to Conducted EMI Estimation in Soft Switching Inverter

Piotr MUSZNICKI ¹⁾, Piotr J. CHRZAN ¹⁾ and Sławomir MANDREK ²⁾

1) Gdańsk University of Technology, Poland

2) Det Norske Veritas Poland Sp. z o.o., Poland

Summary: This paper presents an estimation method of the conducted electromagnetic interference EMI emissions in soft switching inverters. Estimation process is carried out by a number of Wiener filters, which represent different operation conditions as reflected through subsequent power converter states determined by initial commutation event conditions and propagation paths layout. Filters are fed by a semiconductor power switch voltage or current waveforms regarded as sources of perturbation. The EMI emissions are measured on the line impedance stabilization network LISN terminals. Optimal filter adaptation is effected in the frequency domain by measuring input and cross power signal spectra. Analysis of parallel quasi resonant dc link voltage inverter PQRDCLI is outlined to distinguish filters assigned for inverter operation and those for an external DC/DC converter interaction. Experimental results are given to illustrate the Wiener filtering estimation quality. Possibility of detailed decomposition of the LISN-EMI waveforms is depicted in both time and frequency domain. Comparative analysis of frequency responses for PQRDCLI link voltage changes is given.

Key words:
 Wiener filtering,
 estimation,
 electromagnetic
 interference,
 resonant power
 conversion

1. INTRODUCTION

Nowadays, there is a trend for increasing switching frequencies of power semiconductor devices in order to reduce geometrical dimension and weight of power converters. This is unfortunately related with increasing electromagnetic interference (EMI) of conducted and radiated emissions. Many official regulations have been issued for limiting the EMI [15, 16]. Therefore, for optimization of power electronic converters, better understanding and awareness of EMI generation phenomena is needed. Reliable emission estimation at the early stage of converter development, possibly before experimental prototype validation, could drastically reduce production time and improve final design. State of the art evaluation of EMI in power electronics can be effected by:

- Expert knowledge and systems [1–2],
- Simplified modeling in time and frequency [7, 14]
- Accurate computer simulation [3, 8, 10]
- Signal processing and estimation [11]

In this paper, digital signal processing approach exploiting the Wiener filtering has been considered. While in previous papers it has been proved this theory relevance to conducted EMI estimation applied for a *dc/dc* boost converter [12] and for conventional hard-switched voltage source inverter [13]. The paper objective is to focus on the EMI estimation in soft switching inverter, identifying perturbation voltage waveforms across the Line Impedance Stabilization Network (LISN) terminals, sources of perturbation and its propagation paths. The Wiener filtering is applied to estimation by a number of filters corresponding exactly to converter states at different initial conditions (Fig. 1). The definition and detection of subsequent states and commutation between them is crucial for optimal filtering using available for measurement transient voltage- or current-waveforms.

2. WIENER FILTERING

Theory of optimum linear filters for the general case of minimum mean square error criterion was developed by Wiener for continuous time and independently by Kolmogorov for discrete time systems [5, 6]. In this approach, the Wiener filters used for the EMI estimation are fed by power switch voltage or current transients, as represented by the source of disturbances v in Figure 2. Corresponding conducted network emissions p , that can be isolated in the LISN terminals, consist of reconstructed disturbances p_v – available to be estimated with the aid of filtering source v and inevitably present additive noise term p_o .

As the assumptions of Wiener filter theory claim:

- Filter system H is linear and time-invariant,
 - Noise p_o coming from other sources is additive and not correlated with the source of disturbances v
- one can express v and p_v by the Fourier transforms:

$$P_v(j\omega) = H(j\omega) V(j\omega) \quad (1)$$

where $H(j\omega)$ is the filter frequency response.

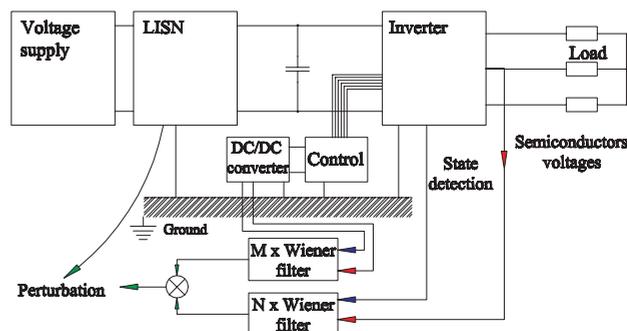


Fig. 1. Wiener filters applied to EMI estimation in the voltage inverter

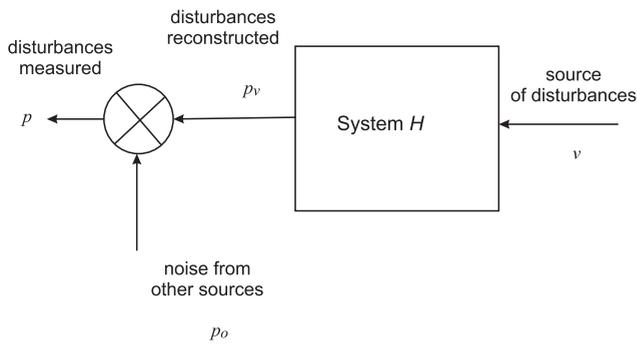


Fig. 2. Wiener filtering in estimation of EMI

The estimation error $E_r(j\omega)$ is defined as the difference between the measured and reconstructed disturbances:

$$E_r(j\omega) = P(j\omega) - H(j\omega)V(j\omega) \quad (2)$$

Then mean square estimation error is given as follows:

$$E \left\{ \left| E_r(j\omega) \right|^2 \right\} = E \left\{ E_r(j\omega)^* E_r(j\omega) \right\} \quad (3)$$

where $E\{\cdot\}$ denotes mean value operator. Using the mean square error criterion, the derivative of (3) with respect to $H(j\omega)$ is calculated from:

$$\frac{\partial}{\partial H(j\omega)} E \left\{ \left| E_r(j\omega) \right|^2 \right\} = 0 \quad (4)$$

Hence, the optimal value of $H(j\omega)$ is the frequency response of the optimal Wiener filter:

$$H(j\omega) = \frac{S_{vp}(j\omega)}{S_{vv}(j\omega)} \quad (5)$$

where $S_{vv}(j\omega) = E\{|V(j\omega)|^2\}$ is the power spectrum of v , and $S_{vp}(j\omega) = E\{P(j\omega)V^*(j\omega)\}$ is the cross power spectrum between v and p . In order to obtain $H(j\omega)$, one must first measure data of input v and output p signals. Once the filter has been determined, it can be used to any form of source disturbance v . To optimally estimate EMI transients p_v , equation (1) should be accompanied with inverse Fourier transform of resulting $P_v(j\omega)$.

3. SOFT SWITCHING INVERTER

The soft switching inverters have less influence on the EMI generation because of lower dv/dt and di/dt , moreover allow decreasing voltage stress across the power semiconductor switches without deterioration of overall efficiency of converter [9]. In this section is described the parallel quasi resonant dc link voltage inverter (PQRDCLI) [17], as is depicted in Figure 3 and in detailed analysis with gating control procedure described in [4]. This circuit allows zero voltage switching (ZVS) for inverter transistors

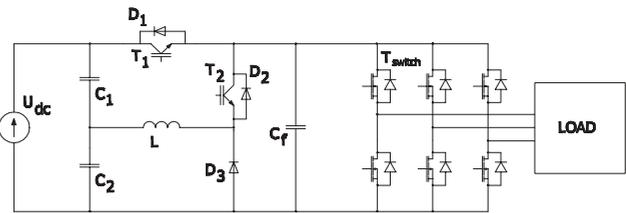


Fig. 3. PQRDCLI circuit topology

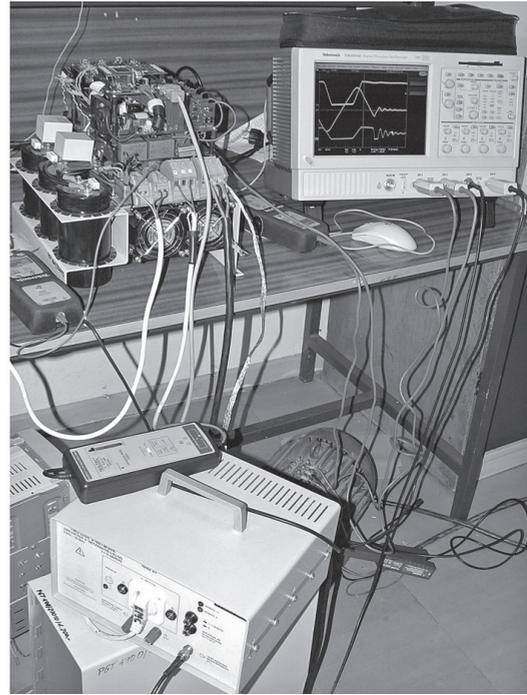


Fig. 4. Laboratory test bench

and also gives ZVS or zero current switching (ZCS) for auxiliary circuit transistors T_1 and T_2 . The parallel quasi resonant circuit is located in the dc link of indirect frequency converter. It consists of two input electrolytic capacitors C_1 , C_2 connected in series. The inverter is separated from dc voltage source by the bilateral switch based on transistor T_1 with diode D_1 .

The capacitors midpoint is connected to the inductor L , to form quasi resonant circuit with the capacitor C_f through auxiliary switching devices: transistor T_2 with diode D_2 or diode D_3 .

The every commutation in the inverter is preceded by reloading resonant circuit in order to discharge of the input capacitor C_f . The main source of EMI of the PQRDCLI is changing of the DC link voltage across input inverter capacitor C_f . However, the rise and fall time of this voltage is much lower than in hard switching inverter. Based on the circuit topology, perturbation propagation paths do not change significantly for various inverter conduction states as has been recognized in the case of hard switched inverter.

This phenomenon can be explained by common zero voltage initial conditions (turn-on state) of all inverter switches when the commutation occurs. Therefore, taking into account major differences in propagation paths when voltage across C_f rises up or falls down, two states ($N = 2$) for the PQRDCLI operation and two states ($M = 2$) for DC/DC external converter have been distinguished.

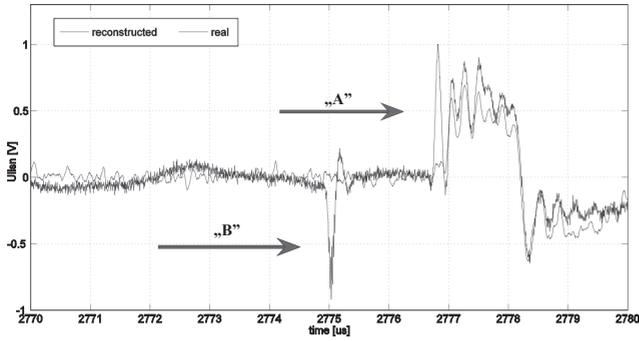


Fig. 5. Comparison of real and estimated LISN-EMI transients („A” — T2 ZCS commutation, „B” — DC/DC converter)

4. EXPERIMENTAL RESULTS

Experimental unit presented in Figure 4 contained the PQRDCLI fed from dc power supply through the LISN (Schaffner NNB41). The prototype inverter was built with Insulated Gate Bipolar Transistors (IGBT). Transistor drivers were supplied from the separate DC/DC converter constituting additional EMI source. Voltage waveforms and perturbations have been registered for different operation conditions using the oscilloscope TDS5034B of Tektronix with voltage differential probes. The sampling frequency was 100 MHz. The symmetrical inverter load was the induction machine (SZJe34a 220/380V 3 kW).

The Wiener filtering method, as described in section II, has been used with data obtained from measurement. In Figure 5 influence of the external DC/DC converter and transistor T2 commutation on total perturbation waveform is shown. These two signals are not correlated with the assigned to the filter dc link voltage disturbance signal; hence the impact of them in reconstructed signal is not observable.

Therefore, adding appropriate number of filters one can separate EMI noise influence from the external DC/DC converter. In Figure 6 with the aid of filter assignments, total LISN perturbations are decomposed among separate sources. The respective signal spectra are depicted in the following Figure 7.

The changing dc link voltage across C_f is the main source of perturbations. Comparing filter frequency response $H(j\omega)$ for the case, when voltage across C_f rises up or falls down in Figure 8 one can proof differences in propagation paths in the frequency range above 10 MHz. It results from the differences of circuit components states and parameters (e.g. semiconductor capacitances) for these two inverter states. Indeed, the falling time of voltage across the input capacitor is shorter than the rising time. It reflects influence on the perturbation currents distribution, changing of a currents density in the conductors, of electric and magnetic couplings. The perturbation spectra generated by inverter, as in Figure 7b can be equally decomposed into two separate frequency transmittances for each inverter state.

5. CONCLUSIONS

Wiener filtering applied to estimation of the conducted EMI emissions in soft switching inverters confirms to be

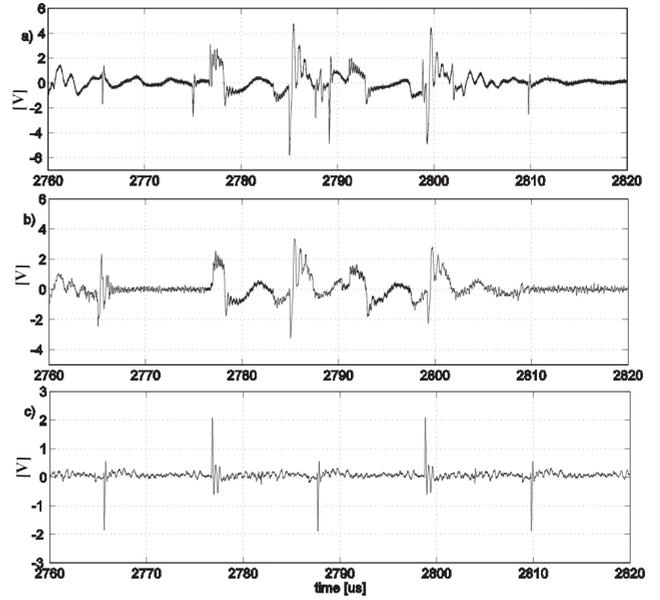


Fig. 6. Estimation and separation of experimental EMI waveforms; a) total LISN-EMI, b) PQRDCL inverter, c) DC/DC external converter estimated contribution

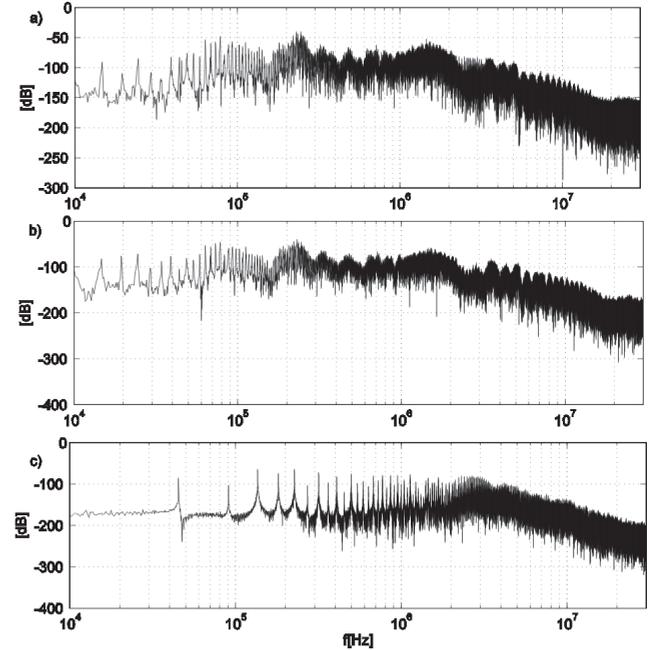


Fig. 7. Estimation and separation of experimental EMI spectra; a) total LISN-EMI spectrum, b) PQRDCL inverter part; c) DC/DC external converter estimated contribution

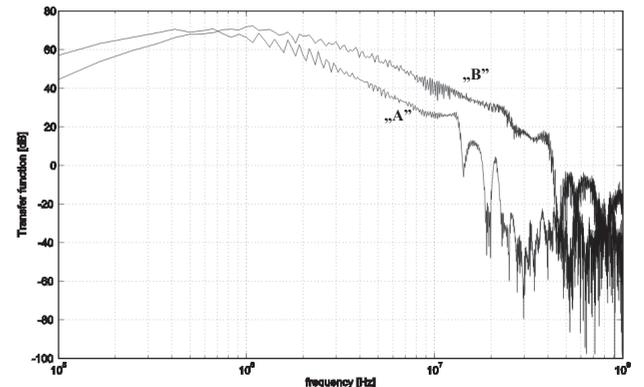


Fig. 8. Comparison of Wiener filters frequency response for rising (marked „A”) and falling (marked „B”) input inverter voltage.

an efficient identification technique as has been previously tested for the case of hard switching inverter and DC/DC boost converter.

The proper selection of different EMI propagation conditions defining minimal number of converter states with its propagation path layout reflects a necessary number of the Wiener filters. Such an estimation structure gives a powerful insight into EMI distribution. Moreover, it enables to decompose the EMI transients indicating its origin sources contribution and evaluating its propagation transfer function.

In the particularly examined method application to the PQRDCLI: two filters for DC link voltage changes and two filters for DC/DC converter operation have proved the efficient EMI estimation.

ACKNOWLEDGMENT

The authors gratefully acknowledge the contributions of J.-L. Schanen and P. Granjon from the Grenoble Institut of Technology for their helpful discussion and initialization of the paper research objectives.

REFERENCES

1. Skibinski G. L., Kerkman K.L., Schlegel D.: *EMI emissions of modern PWM ac drives*. IEEE Industry Applications Magazine, Nov/Dec 1999, vol. 5, pp. 47–80.
2. Sroka J.: *Radio frequency unintentional emission from arrangements of power electronics*. 2005; Przegląd Elektrotechniczny, vol. 81, No 4, pp. 7–16.
3. Lai J.S., Huang X., Pepa E., Chen S., Nehl T.W.: *Inverter EMI modeling and simulation methodologies*. IEEE Trans. Industrial Electronics, 2006, vol. 53, No 3 pp. 736–744.
4. Mandrek S., Chrzan P.J.: *Quasi-resonant dc link inverter with reduced number of active elements*. IEEE Trans. Industrial Electronics, vol. 54, No 4, Aust 2007, pp. 2088–2094.
5. Vaseghi S.V.: *Advanced digital signal processing and noise reduction*. 2000, John Wiley and Sons Ltd.
6. Manolakis D.G., Ingle V.K., Kogan S.M.: *Statistical and adaptive signal processing*. Artech House, Boston-London, 2005, pp. 261–331.
7. Musznicki P., Mandrek S., Chrzan P.J., Jamet J.: *Modeling of PCB layout parasitic for resonant DC link inverter*. in Proc. 2005 Summer Seminar on Nordic Network for Multi Disciplinary Optimized Electric Drives NorMUD, pp. 71–74. Available: <http://normud.iet.aau.dk>.
8. Władziński W., Chrzan P.J., Musznicki P.: *Modeling of conducted emission of dc-dc switch-mode converter*. In Proc. 2003 International Workshop Compatibility in Power Electronics CPE, pp. 240–247.
9. Mandrek S, Chrzan P.J.: *Critical evaluation of resonant dc voltage link inverters for electrical drives*. In Proc. 2003 International Workshop Compatibility in Power Electronics CPE, p. 1–6.
10. Musznicki P., Schanen J.L., Allard B., Chrzan P.J.: *Accurate modeling of layout parasitic to forecast EMI emitted from a DC-DC converter*. In Proc. 2004 IEEE Power Electronics Specialists Conf. PESC, pp. 278–283.
11. Kempski A.: *EMI noise splitting into common and differential modes in PWM inverter drive system*. In Proc. 2005 International Workshop Compatibility in Power Electronics CPE, p. 1–4.
12. Musznicki P., Schanen J.L., Granjon P., Chrzan P.J.: *Better understanding EMI generation of power converters*. In Proc. 2005 IEEE Power Electronics Specialists Conf. PESC, pp. 1052–1056.
13. Musznicki P., Schanen J.L., Granjon P., Chrzan P.J.: *EMI estimation for DC/AC hard switching converter using Wiener filter*. In Proc. 2006 Power Electronics and Motion Control Conference EPE-PEMC, p. 1–6.
14. Teulings W.: *Prise en compte du câblage dans la conception et la simulation des convertisseurs de puissance*. Ph.D. dissertation, INPG, Grenoble 1997.
15. EN 61000-3-2:2006, *Electromagnetic compatibility (EMC) Limits for harmonic current emissions*. European Committee for Electrotechnical Standardization, (CENELEC).
16. EN 61000-6-4:2007, *Electromagnetic compatibility (EMC) Generic standards — Emission standard for industrial environments*. CENELEC.
17. Mandrek S., Chrzan P.J.: *Indirect frequency converter with quasi-resonant dc link circuit*. (in polish) Biuletyn Urzędu Patentowego Nr 16, Patent Application P-372473, Gdańsk University of Technology Poland, Warszawa 2006.



Piotr J. Chrzan

was born in Sopot, Poland, in 1954. He received the M.Sc., Ph.D., and Dr.Sc. degrees in 1978, 1988, and 1999, respectively, all from the Gdańsk University of Technology. Since 1980, he has been with the Faculty of Electrical and Control Engineering of the Gdańsk University of Technology. He visited several times the Laboratoire d'Electrotechnique de Grenoble (LEG) as invited researcher. He has held several visiting professor positions at the Institut National Polytechnique de Toulouse (LEEI-ENSEEIH) and the University of Poitiers (LAI-ESIP). He is currently a Professor and Head of the Chair of Power Electronics and Electrical Machines at the Gdańsk University of Technology. His research interests are in aerospace electrical systems, modeling and control of electrical machines, power converters, and electromagnetic compatibility. He is a member of the Editorial Committee of the Revue Internationale de Génie Electrique (Hermes-Lavoisier).

Address:

Faculty of Electrical and Contr. Engineering,
Gdańsk University of Technology,
ul. Narutowicza 11/12, 80-952 Gdańsk, Poland;
e-mail: p.musznicki@ely.pg.gda.pl, pchrzan@ely.pg.gda.pl.



Piotr Musznicki

was born in Slupsk, Poland, in 1976. He received the M.Sc. and Ph.D. degrees in Power Electronics from the Faculty of Electrical and Control Engineering of the Gdańsk University of Technology in 2001 and 2006 and also Ph.D. degree in INPG Laboratoire d'Electrotechnique de Grenoble in 2006. Presently, he works in Power Electronics and Electrical Machines at the Gdańsk University of Technology. His research interests are in electromagnetic compatibility, modeling and control of power electronics converters and electrical machines, and digital signal processing.

Address:

Faculty of Electrical and Contr. Engineering,
Gdańsk University of Technology,
ul. Narutowicza 11/12, 80-952 Gdańsk, Poland;
e-mail: p.musznicki@ely.pg.gda.pl, pchrzan@ely.pg.gda.pl.



Sawomir Mandrek

was born in Lidzbark Warmiński, Poland, in 1969. He received the M.Sc. degree in Power Electronics from the Faculty of Electrical and Control Engineering of the Gdańsk University of Technology in 1994. From 1996 to 2001, he was with the Electrotechnical Institute – Gdańsk Branch. In 2001, he joined the Gdańsk Shiprepair Yard „Remontowa” S.A., Since 2006 he has been working for Det Norske Veritas as an approval engineer. Presently working toward the Ph.D. degree in power electronics at the Gdańsk University of Technology, Poland. His research interests include power electronics, motion control, new topologies of power converters and computer simulation.

Address:

Det Norske Veritas Poland Sp z o.o.,
ul. 3 Maja 67/69, 81-850 Sopot, Poland;
e-mail: slawomir.mandrek@dnv.com.

This work was supported in part by the Polish Committee for Scientific Research under Grant 3 T10A 038 28